



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al. Art Unit : 2825
Serial No. : 09/941,519 Examiner : Annette M. Thompson
Filed : August 29, 2001
Title : GATE ESTIMATION PROCESS AND METHOD

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

This filing is being made with the filing of a Request for Continued Examination.

Respectfully submitted,

Date: _____

3/18/07

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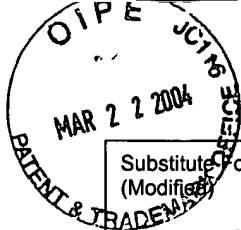
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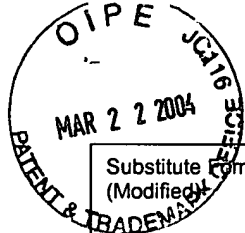
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-605001	Application No. 09/941,519
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
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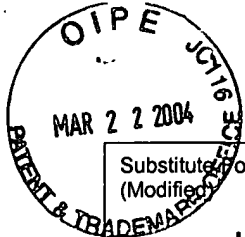
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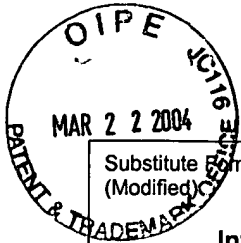


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	AZZZ	WO 98/55879	12/1998	WIPO				

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							Yes	No
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